

# Abstracts

## Exact Group Delay Sensitivities for Minimization of Delay and Distortion in High-Speed VLSI Interconnects

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*R. Liu, Q.J. Zhang and M.S. Nakhla. "Exact Group Delay Sensitivities for Minimization of Delay and Distortion in High-Speed VLSI Interconnects." 1992 MTT-S International Microwave Symposium Digest 92.2 (1992 Vol. II [MWSYM]): 833-836.*

Efficient computation of exact group delay and its sensitivities w.r.t. design parameters in multiconductor transmission line networks is presented. This method is combined with minimax optimization to perform gradient based minimization of delay and distortion in high-speed VLSI interconnects.

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